

FIG. 1A

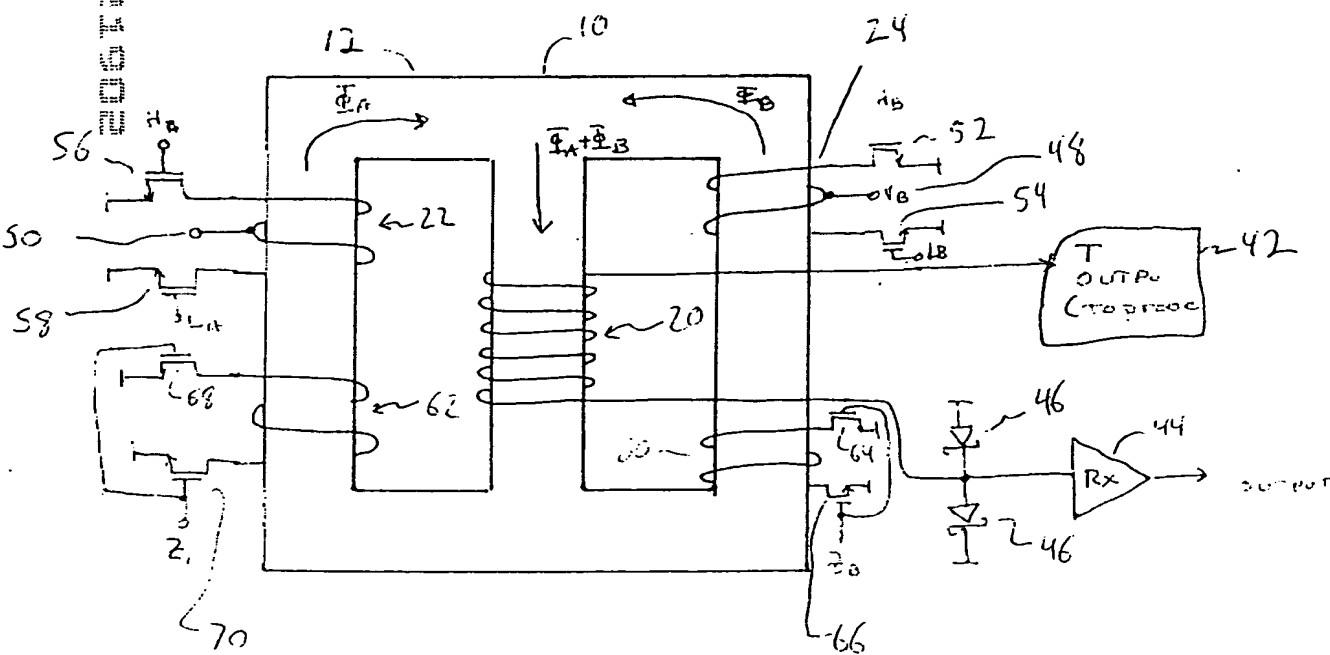


Figure 3

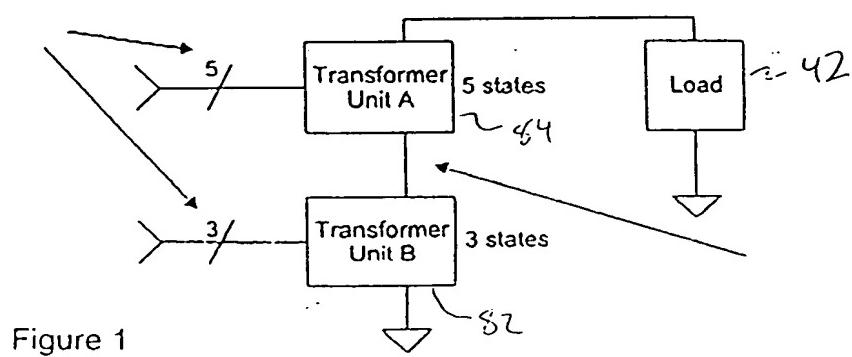
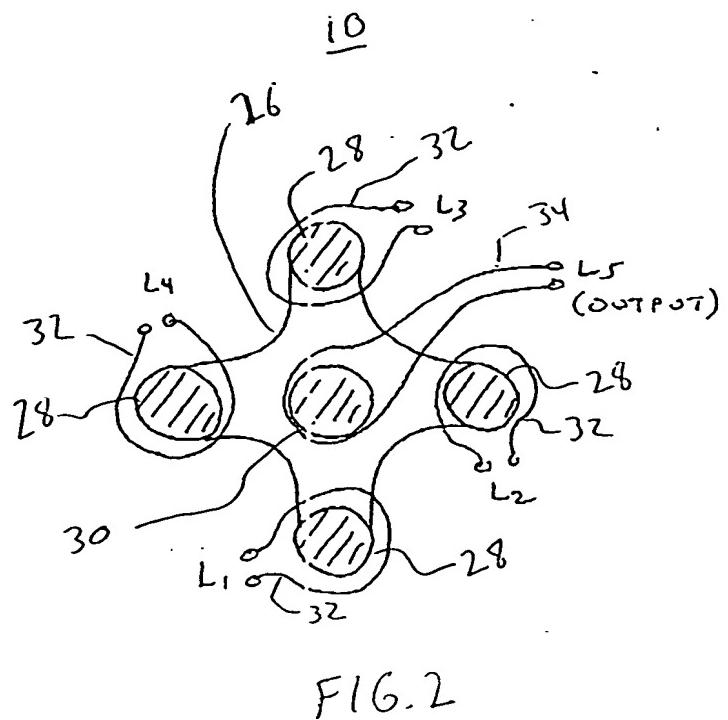


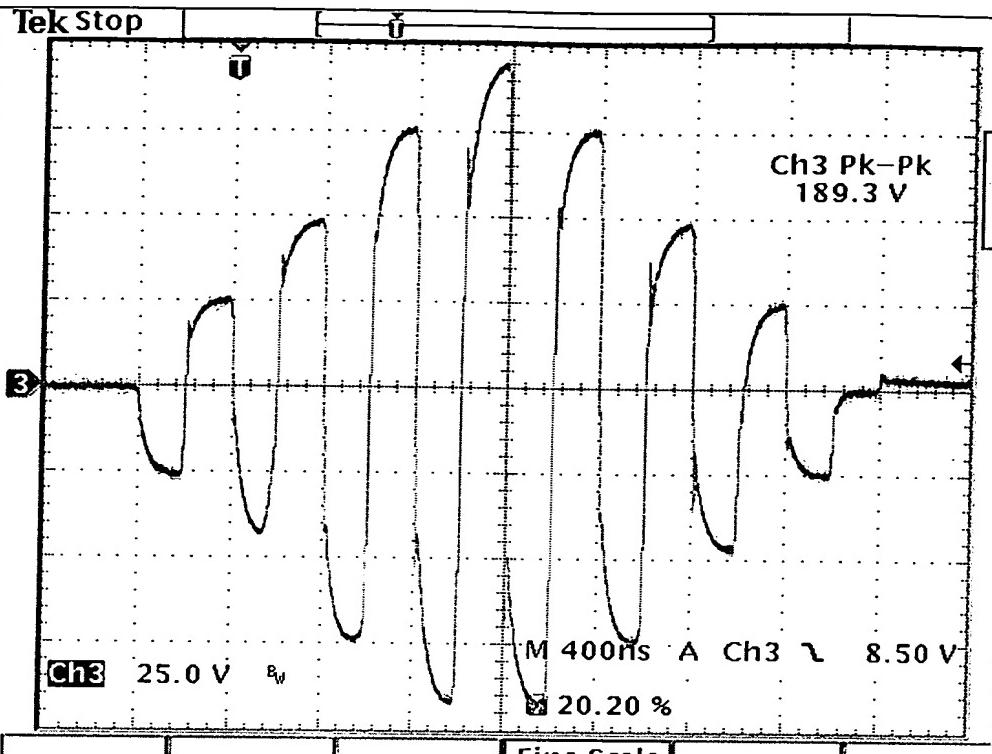
FIG. 6

9-Level TX
(50Ω Load)

$F=2.5$ MHz

$V_b=15V$
 $V_a=5V$

Figure 5



H2

L2

Z2

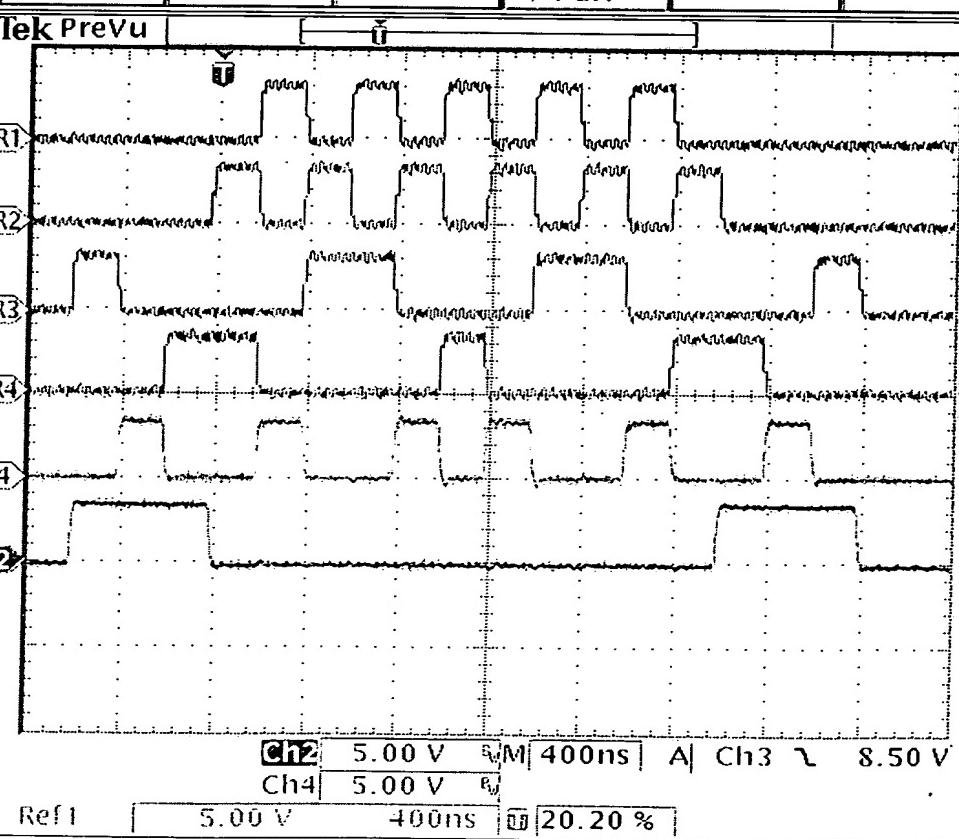
H1

L1

Z1

Control Logic

Figure 4



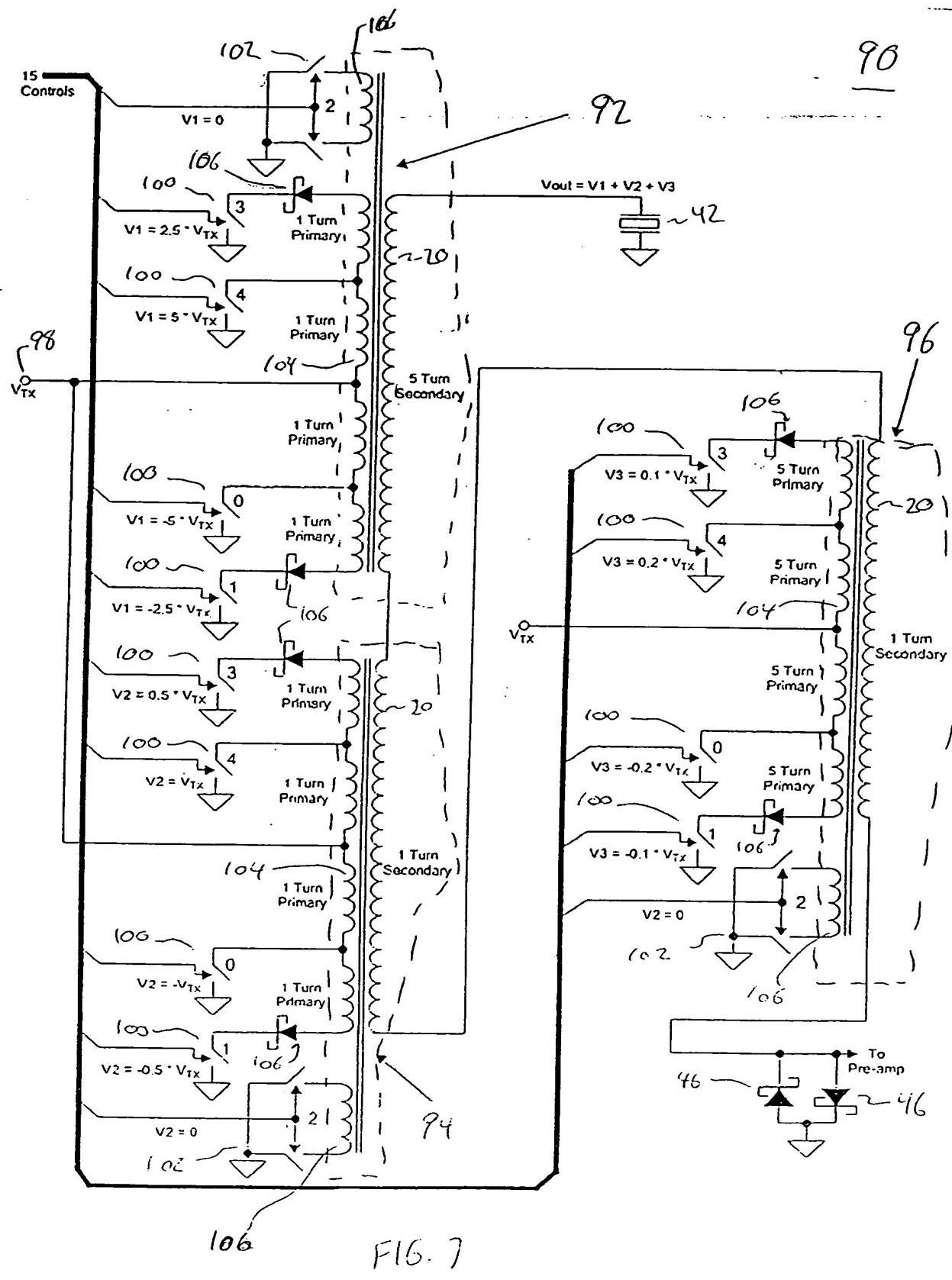


FIG. 7